

Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DKT. NO. 501.33873VC4	SERIAL NO.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		APPLICANT KAWAGOE, et al.	
		FILING DATE December 14, 2001	GROUP

U.S. PTO
10/01/2001
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U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
AW	AA 4,835,740	5-89	Sato			
	AB 5,290,714	3-94	Onozawa			
	AC 4,761,384	8-88	Neppi			
	AD 5,156,990	10-92	Mitchell			
	AE 6,043,114	3-00	Kawagoe			
	AF 4,564,416	1-86	Homma, et al.			
	AG 4,684,971	8-87	Payne, et al.			
	AH 5,237,188	8-93	Iwai, et al.			
	AI 5,396,093	3-95	Lu			
	AJ 5,508,540	4-96	Ikeda, et al.			
	AK 5,508,549	4-96	Watanabe, et al.			
AW	AL 3,974,003	8-76	Zirinsky, et al.			

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Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation / Abstract	
						Yes	No
AW	AM 0373723	6-20-90	Europe				
	AN 0550021	7-85	Europe				
	AO 60132358	7-85	Japan				
	AP 58218159	12-83	Japan				
	AQ 63157477	6-88	Japan				
AW	AR 61002356	1-86	Japan				
AW	AS						
AW	AT						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AW	AU	Wolf, S., "Silicon Processing for the VLSI Era Vol. 1", ppg. 64-65.
AW	AV	Ghandhi, S., "VLSI Fabrication Principles Silicon and Gallium Arsenide", ppg. 735-738.
AW	AW	Yamaguchi, et al., "Process integration and device performance of a submicrometer BiCMOS with 16-GHz ft(f) double Poly-Bipolar devices", IEEE Transactions on Electron Devices, Vol. 36, No. 5, ppg. 890-896.
	AX	
	AY	
	AZ	
Examiner		Date Considered
[Signature]		6/14/01

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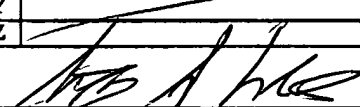
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AA	5,216,269	6-93	Middlehoek, et al.			
AB	4,525,920	7-85	Jacobs, et al.			
AC	4,717,686	1-88	Jacobs, et al.			
AD	4,766,090	8-88	Coquin, et al.			
AE	5,296,047	3-22-94	Fellner			
AF	4,477,310	10-84	Park, et al.			
AG	4,803,179	2-89	Neppl, et al.			
AH	4,005,453	1-77	Le Can, et al.			
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AJ	4,578,128	3-86	Mundt, et al.			
AK	4,662,082	11-86	Dyson, et al.			
AL						

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